

## Monolithic Amplifier Circuits - Course Syllabus

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**Course Number:** EE 202

**Course Title:** Monolithic Amplifier Circuits

**Academic Semester:** Spring

**Academic Year:** 2015/ 2016

**Semester Start Date:** Jan, 24, 2016

**Semester End Date:** May, 19, 2016

**Class Schedule:** Mon. & Wed. 10:30 – 11:55 AM

**Classroom Number:** 3225 Bldg. 9 Level 3

**Instructor(s) Name(s):** Talal Al-Attar

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**Teaching Assistant name:**

**Email:**

**Office Location:** Bldg:1 Level:3 Office: 3134

### COURSE DESCRIPTION FROM PROGRAM GUIDE

Analysis and design of BJT and MOS multi-transistor amplifiers. Feedback theory and application to feedback amplifiers. Stability considerations, pole-zero cancellation, root locus techniques in feedback amplifiers. Detailed analysis and design of BJT and MOS integrated operational amplifiers. Lectures and laboratory.

### COMPREHENSIVE COURSE DESCRIPTION

This course covers the design, construction, and debugging of monolithic amplifier circuits.

The main contents are: the basic principles of operation, terminal characteristics, and equivalent circuit models for transistors and op-amps. Design and analysis of multistage analog amplifiers, differential amplifiers, and current mirrors. Frequency response of cascaded amplifiers and gain-bandwidth considerations. Feedback, stability and Noise analysis.

Design and Simulation exercises are significant components of the course.

## GOALS AND OBJECTIVES

After completing this course, the student should have a firm grasp on:

- The concepts, the models, the use and the analysis of
  - o MOS transistors in different circuits and applications
  - o Single stage and two stage op-amps in different circuits and applications
- Amplifier stability, feedback and frequency response and compensation

## REQUIRED KNOWLEDGE

Familiarity with Transistors models, small signal analysis and circuit simulation tools.

## REFERENCE TEXTS

Analysis and Design of Analog Integrated Circuits, 5th Edition, Grey et al

Microelectronic Circuits, Sedra/Smith, 7th edition

Design of Analog CMOS Integrated Circuits, Razavi

## METHOD OF EVALUATION

Percentages %	Graded content
20%	Sims
10%	Homeworks
15%	Quizzes
10%	Project/Presentation
25%	Midterms

## COURSE REQUIREMENTS

### Assignments

#### Homework:

Homework assignments are due on Wednesday in the class.

The Solutions will be provided by the end of the day homeworks are due.

If you need more time for your homework, you have to inform the Instructor or the TA in advance.

#### Quizzes:

There will be at least one Quiz every week starting the 2nd week and will be based on a 0-10 point scale. The worst few quizzes will be dropped (If any). If you are keeping up with the problem sets, they should be relatively easy.

#### Exams:

There will be two midterm exams and one final exam

You are free to use your text book, course notes and any provided figures and handouts.

## **Course Policies**

All homework assignments, quizzes, and exams are required. Students who do not show up for a Quiz or an exam should expect a grade of zero on that exam.

If you dispute your grade on any homework, quiz, or exam, you may request a re-grade (from the TA for the homeworks and quizzes or from the instructor for the exams) only within 48 hours of receiving the graded exam.

Incomplete (I) grade for the course will only be given under extraordinary circumstances such as sickness, and these extraordinary circumstances must be verifiable. The assignment of an (I) requires first an approval of the dean and then a written agreement between the instructor and student specifying the time and manner in which the student will complete the course requirements.

## **Additional Information**

Engineers are required to practice “continuous” or “life-long” learning. This course will cover a lot of material which will require the students to do a lot of self-study, reading of the textbooks and handouts, learning how to use equipment and software, etc...Although the instructor and the TAs are committed to help the students in this course, the students are also expected to take initiatives and to get used to this notion of self-study that will be anyway (i) expected from them in their future careers and (ii) imperative to their success and survival in the real engineering and academic worlds.

Please don't hesitate to ask if you have any question or concern about the course.

## **NOTE**

The instructor reserves the right to make changes to this syllabus as necessary.